

Abstract of the Disclosure

A display has a circuit for comparing video data for a first or current frame with video data for a second or desired frame. A processing section makes an instruction for a process to take place if certain conditions are met. In response to the instruction for a process, a signal generating circuit outputs a signal which reduces a degree of modulation or variation by which tone transition is facilitated relatively to a case where pixels are driven on the basis of data output from an ordinary processing section. Thus, by modulating or varying a drive signal to a degree suitable for a case where tone transition is sufficient, a display capable of improving display quality even in a situation where the display is not capable of being driven suitably, can be realized using circuitry of a relatively small scale.